

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	138	first adj1 (variable adj1 delay)	US-PGPUB	OR	ON	2006/10/13 12:49
L2	8	1 same counter	US-PGPUB	OR	ON	2006/10/13 12:50
L3	61094	control adj circuit	US-PGPUB	OR	ON	2006/10/13 12:50
L4	187	3 with (variable adj delay)	US-PGPUB	OR	ON	2006/10/13 12:53
L5	19	4 same counter	US-PGPUB	OR	ON	2006/10/13 12:50
L6	1136	up\$1down adj counter	US-PGPUB	OR	ON	2006/10/13 12:51
L7	6351	ddr or (double adj data adj rate)	US-PGPUB	OR	ON	2006/10/13 12:52
L8	0	7 same 6	US-PGPUB	OR	ON	2006/10/13 12:52
L9	44	7 and 6	US-PGPUB	OR	ON	2006/10/13 12:52
L10	3914	frequency adj divider	US-PGPUB	OR	ON	2006/10/13 12:52
L11	44	10 same 6	US-PGPUB	OR	ON	2006/10/13 12:53
L12	6	11 and (variable adj delay)	US-PGPUB	OR	ON	2006/10/13 12:53
L13	1122	edge adj triggered	US-PGPUB	OR	ON	2006/10/13 13:03
L14	73	13 near4 register	US-PGPUB	OR	ON	2006/10/13 13:03
L15	0	14 and 6	US-PGPUB	OR	ON	2006/10/13 13:03
L16	1141	series adj2 delay	US-PGPUB	OR	ON	2006/10/13 13:03
L17	0	16 same 6	US-PGPUB	OR	ON	2006/10/13 13:04
L18	21	16 and 6	US-PGPUB	OR	ON	2006/10/13 13:04
L19	94	multiplexer same 6	US-PGPUB	OR	ON	2006/10/13 13:04
L20	10	19.clm.	US-PGPUB	OR	ON	2006/10/13 13:04
L21	112	16 and 7	US-PGPUB	OR	ON	2006/10/13 13:05
L22	1	16 same 7	US-PGPUB	OR	ON	2006/10/13 13:05
L23	119	6 same flip\$1flop	US-PGPUB	OR	ON	2006/10/13 13:05
L24	119	23 same counter	US-PGPUB	OR	ON	2006/10/13 13:05
L25	11	23 same register	US-PGPUB	OR	ON	2006/10/13 13:06